

CLAIMS

What is claimed is:

1. A semiconductor device, comprising:
a first carrier substrate;
a first semiconductor chip mounted on the first carrier substrate;
a second carrier substrate;
a second semiconductor chip mounted on the second carrier substrate;
protruding electrodes for connecting the second carrier substrate to the first carrier substrate so that the second carrier substrate is held above the first semiconductor chip; and
a sealant for sealing the second semiconductor chip so as to include a region in which the protruding electrodes are arranged.
2. The semiconductor device according to Claim 1,
wherein the second carrier substrate is fixed to the first carrier substrate so as to be mounted on the first semiconductor chip.
3. The semiconductor device according to Claim 1,
wherein the sealant further comprises a molded resin.
4. The semiconductor device according to Claim 1,
wherein a position of a sidewall of the sealant coincides with a sidewall of the second carrier substrate.
5. The semiconductor device according to Claim 1,

wherein the first semiconductor chip is flip-chip mounted on the first carrier substrate.

6. The semiconductor device according to Claim 1,
wherein a plurality of the first semiconductor chips is provided in parallel on the first carrier substrate.

7. The semiconductor device according to Claim 6,
wherein the first semiconductor chips are connected to the first carrier substrate by pressure welding.

8. The semiconductor device according to Claim 1,
wherein, at the same temperature, an elastic modulus of a semiconductor device including the first carrier substrate and the first semiconductor chip mounted on the first carrier substrate is different from an elastic modulus of a semiconductor device including the second carrier substrate and the second semiconductor chip mounted on the second carrier substrate.

9. The semiconductor device according to Claim 1,
wherein the first carrier substrate on which the first semiconductor chip is mounted further comprises a flip-chip-mounted ball grid array, and
wherein the second carrier substrate on which the second semiconductor chip is mounted further comprises at least one of a mold-sealed ball grid array and a chip size package.

10. A semiconductor device, comprising:
a carrier substrate;
a first semiconductor chip mounted on the carrier substrate;
a second semiconductor chip mounted on the carrier substrate;
protruding electrodes for connecting the second semiconductor chip to the carrier substrate so that the second semiconductor chip is held above the first semiconductor chip; and
a sealant for sealing the second semiconductor chip so as to include a region in which the protruding electrodes are arranged.
11. The semiconductor device according to Claim 10,
wherein the second semiconductor chip comprises a plurality of stacked semiconductor chips.
12. The semiconductor device according to Claim 10,
wherein the second semiconductor chip comprises a plurality of semiconductor chips mounted in parallel on the second carrier substrate.
13. An electronic device, comprising:
a first carrier substrate;
a first electronic part mounted on the first carrier substrate;
a second carrier substrate;
a second electronic part mounted on the second carrier substrate;

protruding electrodes connecting the second carrier substrate to the first carrier substrate so that the second carrier substrate is held above the first electronic part; and

a sealant sealing the second electronic part so as to include a region in which the protruding electrodes are arranged.

14. An electronic apparatus, comprising:

a first carrier substrate;

a first semiconductor chip mounted on the first carrier substrate;

a second carrier substrate;

a second semiconductor chip mounted on the second carrier substrate;

protruding electrodes connecting the second carrier substrate to the first carrier substrate so that the second carrier substrate is held above the first semiconductor chip;

a sealant sealing the second semiconductor chip so as to include a region in which the protruding electrodes are arranged; and

a mother substrate on which the first carrier substrate is mounted.

15. A method of manufacturing a semiconductor device, comprising the steps of:

flip-chip mounting a first semiconductor chip on a first carrier substrate;

mounting a second semiconductor chip on a second carrier substrate in which an arrangement region for arranging protruding electrodes is provided;

sealing the second semiconductor chip with a sealing resin so that the sealing resin is attached to the region for arranging the protruding electrodes; and

connecting the second carrier substrate to the first carrier substrate via the protruding electrodes so that the second carrier substrate is held above the first semiconductor chip.

16. The method of manufacturing a semiconductor device according to Claim 15, wherein the step of sealing the second semiconductor chip with the sealing resin comprises the steps of:

integrally molding a plurality of the second semiconductor chips, which are mounted on the second carrier substrate, with the sealing resin; and

cutting the second carrier substrate molded with the sealing resin into pieces so that each piece includes one of the second semiconductor chips.

17. A method of manufacturing an electronic device, comprising the steps of:

mounting a first electronic part on a first carrier substrate;

mounting a second electronic part on a second carrier substrate in which an arrangement region for arranging protruding electrodes is provided;

sealing the second electronic part with a sealing resin so that the sealing resin is attached to the region for arranging the protruding electrodes; and

connecting the second carrier substrate to the first carrier substrate via protruding electrodes so that the second carrier substrate is held above the first electronic part.

18. The semiconductor device according to Claim 1,

wherein the second semiconductor chip comprises a plurality of stacked semiconductor chips.

19. The semiconductor device according to Claim 1,
wherein the second semiconductor chip comprises a plurality of semiconductor chips mounted in parallel on the second carrier substrate.